



END SEMESTER EXAMINATION TIMETABLE, MAY/JUNE, 2026
BACHELOR OF TECHNOLOGY (B. Tech.) All Branches
FIRST YEAR, SEMESTER – II (For 2025-26 Batch Under Autonomy)

Date: 21^h May, 2026

Course Name: Engineering Graphics

Day & Date	DIV / Batch	Roll No.		Reporting Time	Examination Time	Venue
		From	To			
01-06-2026 (Monday)	E1 (All Batches)	E001	E062	08:10 am	08:30 am to 10:30 am	Advanced Computing Lab (2nd floor)
	E2 (All Batches)	E063	E124 & E188	11:10 am	11:30 am to 01:30 pm	
02-06-2026 (Tuesday)	E3 (All Batches)	E125	E187	08:10 am	08:30 am to 10:30 am	
	A1 (All Batches)	A001	A063	11:10 am	11:30 am to 01:30 pm	
	A2 (All Batches)	A064	A127	02:10 pm	02:30 PM to 04:30 pm	
03-06-2026 (Wednesday)	S (All Batches)	S001	S063	08:10 am	08:30 am to 10:30 am	
	B (All Batches)	B001	B064	11:10 am	11:30 am to 01:30 pm	
04-06-2026 (Thursday)	M1 (All Batches)	M001	M063	08:10 am	08:30 am to 10:30 am	
	M2 (All Batches)	M064	M126	11:10 am	11:30 am to 01:30 pm	
	M3 (All Batches)	M127	M189	02:10 pm	02:30 pm to 04:30 pm	

All students must strictly observe the reporting time.

Important Note: Changes if any, in the timetable, shall be communicated on the College website (<https://www.djsce.ac.in>).

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Ms. Alka Shukla
Controller of Examinations

Dr. Hari Vasudevan
Principal

22/05/26